**Analog IC Design Project**

Important Links:

* **gm/ID method:**
  + <http://www.designers-guide.org/Forum/Attachments/Gm_BY_ID_Methodology.pdf>
  + <https://www.researchgate.net/post/How_do_I_design_an_amplifier_using_gm_id_methodology>
  + [sci-hub.tw/10.1109/4.535416](https://sci-hub.tw/10.1109/4.535416)